

ABSTRACT

A reflective LCD array (10) having a plurality of mirrors (40) in a mirror layer (14) of material layers (12). Underlying the mirror layer (12) are at least a second metal layer (18) and a third metal layer (20). The second metal layer (18) and the third metal layer (20) will each have a first plurality of power traces (50) and (51), arrayed such that the power traces (50) and (51) generally completely underlie a plurality of gaps (44, 46) between the mirrors (40). The power traces (50) are positioned to shield light (30) from reaching a plurality of silicon junctions (26) where traces on a poly layer (22) intersect traces on a diffusion layer (24).

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